

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Doris et al.

Application No.: Not Assigned

Group Art Unit: Not Assigned

Date Filed: Concurrently

Examiner: Not Assigned

For: **STRUCTURE AND METHOD TO ENHANCE BOTH NFET AND PFET PERFORMANCE USING DIFFERENT KINDS OF STRESSED LAYERS**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Under the provisions of 37 C.F.R. 1.97 through 1.98 and pursuant to applicant's duty of disclosure under 37 C.F.R. 1.56, applicant respectfully brings the following documents listed in the attached from PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents is provided herewith for the convenience of the Examiner.

The present Information Disclosure Statement is being filed no later than three months from the application's filing date and/or before the mailing date of an Office Action on the merits, whichever is later.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the documents be considered by the Examiner and formally made of record in this application.

Respectfully submitted,



Marshall M. Curtis  
Reg. No. 33,138

Whitham, Curtis & Christofferson, P.C.  
11491 Sunset Hills Road, Suite 340  
Reston, VA 20190  
(703) 787-9400  
**CUSTOMER NUMBER 30743**

**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.